



**Australian Government**

**Assessment Requirements for UEEEC0010  
Design and develop advanced digital  
systems**

**Release: 1**

# Assessment Requirements for UEEEC0010 Design and develop advanced digital systems

## Modification History

Release 1. This is the first release of this unit of competency in the UEE Electrotechnology Training Package.

## Performance Evidence

Evidence required to demonstrate competence in this unit must be relevant to and satisfy all of the requirements of the elements, performance criteria and range of conditions on at least two separate occasions and include:

- applying relevant work health and safety (WHS)/occupational health and safety (OHS) requirements, workplace procedures and practices, including using risk control measures
- applying sustainable energy principles and practices
- constructing and testing prototype devices and circuits in accordance with design brief and regulatory requirements
- dealing with unplanned events/situations in accordance with workplace procedures in a manner that minimises risk to personnel and equipment
- developing outlines of alternative designs
- developing the design within the safety and functional requirements and budget limitations
- documenting and presenting design effectively
- negotiating design alteration requests successfully
- obtaining approval for final design
- verifying compliance of the design against the final brief.

## Knowledge Evidence

Evidence required to demonstrate competence in this unit must be relevant to and satisfy all of the requirements of the elements, performance criteria and range of conditions and include knowledge of:

- analogue to digital conversion
- connection of test/measuring devices into a circuit
- current types of oscillators based on their characteristics and operation
- current memory and storage devices based on their characteristics and operation
- design of complex sequential logic circuits (as a minimum three level sequential circuits), including current techniques - equation writing, reduction and propagation delay calculation
- design techniques for combinational and sequential logic circuits (as a minimum three levels and four input circuits)
- digital system design and development requirements

- digital to analogue conversion
- programmable logic devices
- relevant manufacturer specifications
- relevant job safety assessments or risk mitigation processes and electronic safe working practices
- relevant WHS/OHS legislated requirements
- relevant workplace documentation
- relevant workplace policies and procedures.

## Assessment Conditions

Assessors must hold credentials specified within the Standards for Registered Training Organisations current at the time of assessment.

Assessment must satisfy the Principles of Assessment and Rules of Evidence and all regulatory requirements included within the Standards for Registered Training Organisations current at the time of assessment.

Assessment must occur in suitable workplace operational situations where it is appropriate to do so; where this is not appropriate, assessment must occur in simulated suitable workplace operational situations that replicate workplace conditions.

Assessment processes and techniques must be appropriate to the language, literacy and numeracy requirements of the work being performed and the needs of the candidate.

Resources for assessment must include access to:

- a range of relevant exercises, case studies and/or simulations
- relevant and appropriate materials, tools, equipment and personal protective equipment (PPE) currently used in industry
- resources that reflect current industry practices in relation to designing and developing advanced digital systems
- applicable documentation, including workplace procedures, equipment specifications, regulations, codes of practice and operation manuals.

## Links

Companion Volume implementation guides are found in VETNet - -

<https://vetnet.gov.au/Pages/TrainingDocs.aspx?q=b8a8f136-5421-4ce1-92e0-2b50341431b6>